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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO. CONFIRMATION NO.		
10/737,421	12/16/2003	Wolfgang Burkhardt	AO720B	6313	
7590 07/13/2005			EXAMINER		
Arthur G. Schaier			PHAN, THANH S		
Carmody & Tor	rance LLP		·····		
P.O. Box 1110		ART UNIT	PAPER NUMBER		
50 Leavenworth Street			2841		
Waterbury, CT 06721-1110			DATE MAILED: 07/13/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	No.	Applicant(s)				
Office Action Commence		10/737,421		BURKHARDT ET AL.				
	Office Action Summary	Examiner		Art Unit				
		Thanh S. Ph		2841				
Period fo	The MAILING DATE of this communicator Reply	ation appears on the c	over sheet with the c	orrespondence ad	ddress			
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNIC, unsions of time may be available under the provisions of SIX (6) MONTHS from the mailing date of this commun to period for reply specified above is less than thirty (30) of period for reply is specified above, the maximum statuture to reply within the set or extended period for reply will reply received by the Office later than three months after ed patent term adjustment. See 37 CFR 1.704(b).	ATION. 37 CFR 1.136(a). In no event ication. days, a reply within the statuto ory period will apply and will a	t, however, may a reply be timery minimum of thirty (30) days expire SIX (6) MONTHS from ation to become ABANDONE	nely filed s will be considered time the mailing date of this o	ely. communication.			
Status								
1)[🛛	Responsive to communication(s) filed	on <i>04 May 2005</i> .						
2a)⊠	This action is <b>FINAL</b> . 2b	)☐ This action is no	n-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposit	ion of Claims							
4)⊠	Claim(s) <u>1-8</u> is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)	)□ Claim(s) is/are allowed. )☑ Claim(s) <u>1-8</u> is/are rejected.							
6)⊠								
7)								
8)	Claim(s) are subject to restriction	n and/or election req	uirement.					
Applicat	ion Papers							
9)[	The specification is objected to by the E	Examiner.						
10)[	☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11)	The oath or declaration is objected to b	y the Examiner. Note	the attached Office	Action or form P	TO-152.			
Priority ι	under 35 U.S.C. § 119							
	Acknowledgment is made of a claim for ☐ All b)☐ Some * c)☐ None of:			-(d) or (f).				
	1. Certified copies of the priority do							
	2. Certified copies of the priority do							
	3. Copies of the certified copies of			ed in this National	Stage			
* 0	application from the Internationa	·						
`	See the attached detailed Office action f	or a list of the certific	a copies not receive	d.				
Attachmen	t(s)							
_	e of References Cited (PTO-892)	4	) Interview Summary	(PTO-413)				
	e of Draftsperson's Patent Drawing Review (PTO	-948)	Paper No(s)/Mail Da	ite	0.450)			
	mation Disclosure Statement(s) (PTO-1449 or PT r No(s)/Mail Date		i)	atent Application (PT)	O-162)			

#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ellner et al. [US 6,618,328] in view of Schartz [US 5,210,722].

Regarding claims 1 and 5-8, Ellner et al. dislcose an electronic device [1] that includes functionality to perform at least two functions, a first of which is at least timekeeping and the other of which is at least a function other than timekeeping, wherein the electronic device comprises a multilayered module comprising: a frame [not explicitly labeled; the frame is necessarily for comprising the electronic device housing structure] having a frame surface; a timepiece [23] and a removable (second) printed circuit board including means related to at least the function other than timing [an digital and/or an auditory message which obviously comprise a circuit board in order to perform digital and/or electronically functions; column 2, line 58 — column 3, line 2] wherein the second printed circuit board is securable within the module [the replaceable and exchangeable digital or auditory message is secured in the device structure].

Ellner et al. disclose the timepiece except for wherein the timepiece comprise a circuit board/first board and a compartment dimensioned for receiving the battery.

wherein the battery compartment is asymmetrically positioned within the module thus creating a region above a printed circuit board and proximate the battery compartment.

Schwartz discloses an analog timepiece comprising a compartment dimensioned for receiving a battery [energy cell 30], wherein the battery compartment is asymmetrically positioned within the module thus creating a region above a printed circuit board [11] and proximate the battery compartment.

It would have been obvious to one of ordinary skill in the art at the time of the invention was made to use the timepiece design of Schwartz with Ellner et al. for the purpose of facilitating power to circuit arrangement within a compact space.

Regarding claims 2-4, Ellner et al. disclose that the second printed circuit board secured within the compartment of the electronic device, and Schartz discloses that the first circuit board is secured within the timepiece. Furthermore, the circuit board(s) and other electronic components should be electrically and physically supported by supporting member(s) within the electronic device in order for the device to perform its functions accordingly.

## Response to Arguments

Applicant's arguments with respect to claims 1-8 have been considered but are moot in view of the new ground(s) of rejection.

### Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP

§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thanh S. Phan whose telephone number is 571-272-2109. The examiner can normally be reached on M-F 9:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

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you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

tsp

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800